

SEMiX[®] 5

Trench IGBT Modules

Engineering Sample SEMiX305GARL07E3

Target Data

Features

- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

Typical Applications*

- UPS
- 3 Level Inverters

Remarks

- Case temperature limited to T_C=125°C max.
- Product reliability results are valid for $T_{jop}=150^{\circ}C$
- Dynamic data are estimated
- For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"



Absolute	Maximum Rating	s			
Symbol	Conditions		Values	Unit	
IGBT					
V _{CES}	T _j = 25 °C		650	V	
lc	T 175 %0	T _c = 25 °C	353	А	
	_ T _j = 175 °C	T _c = 80 °C	265	Α	
I _{Cnom}		1	300	Α	
I _{CRM}	$I_{CRM} = 3 x I_{Cnom}$		900	Α	
V _{GES}			-20 20	V	
t _{psc}	$V_{CC} = 360 V$ $V_{GE} \le 15 V$ $V_{CES} \le 650 V$	T _j = 150 °C	6	μs	
Tj			-40 175	°C	
Inverse d	iode				
V _{RRM}	T _j = 25 °C		650	V	
l _F	T _j = 175 °C	T _c = 25 °C	86	A	
		T _c = 80 °C	64	A	
I _{Fnom}		_	50	A	
I _{FRM}	$I_{FRM} = 2xI_{Fnom}$		100	А	
I _{FSM}	$t_p = 10 \text{ ms}, \sin 180^\circ, T_j = 25 ^\circ\text{C}$		550	А	
Tj			-40 175	°C	
Freewhee	ling diode				
V _{RRM}	T _j = 25 °C		650	V	
I _F	T _j = 175 °C	T _c = 25 °C	355	А	
		T _c = 80 °C	259	A	
I _{Fnom}		<u>.</u>	300	А	
I _{FRM}	$I_{FRM} = 2 x I_{Fnom}$		600	Α	
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		2160	Α	
Tj			-40 175	°C	
Module	-			•	
I _{t(RMS)}			400	А	
T _{stg}	module without TI	N	-40 125	°C	
V _{isol}	AC sinus 50Hz, t =	1 min	4000	V	

Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
V _{CE(sat)}	$I_{C} = 300 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T _j = 25 °C	1	1.45	1.90	V
		T _j = 150 °C		1.69	2.10	V
V _{CE0}	chiplevel	T _j = 25 °C		0.90	1.00	V
		T _j = 150 °C		0.82	0.90	V
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		1.83	3.0	mΩ
	chiplevel	T _j = 150 °C		2.9	4.0	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}, I_C = 4.8$	mA	5.1	5.8	6.4	V
I _{CES}	V _{GE} = 0 V V _{CE} = 650 V	T _j = 25 °C		0.10	0.3	mA
		T _j = 150 °C		-		mA
Cies	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		18.5		nF
Coes		f = 1 MHz		1.16		nF
C _{res}		f = 1 MHz		0.55		nF
Q _G	V _{GE} = - 15 V+ 15 V			3940		nC
R _{Gint}	T _j = 25 °C			1.0		Ω



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Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
t _{d(on)}	V _{CC} = 300 V	T _i = 150 °C	1	220		ns
t _r	$I_{\rm C} = 300 {\rm A}$	T _i = 150 °C		220		ns
Eon	$V_{GE} = +15/-15 V$ $R_{G \text{ on}} = 13 \Omega$	T _j = 150 °C	21.3 1120			mJ
t _{d(off)}	$R_{G off} = 13 \Omega$	T _j = 150 °C				ns
t _f	di/dt _{on} = 2038 A/µs	T _j = 150 °C		103		ns
E _{off}	di/dt _{off} = 3960 A/μs du/dt = 3052 V/μs	T _j = 150 °C		21.3		mJ
R _{th(j-c)}	per IGBT				0.18	K/W
R _{th(c-s)}	per IGBT (λgrease thickness 50-100µr			0.066		K/W
R _{th(c-s)}	per IGBT (λ=3.4 W	/mK)		t.b.d.		K/W
Inverse d	iode					
$V_F = V_{EC}$	I _F = 50 A	T _j = 25 °C		1.37	1.73	V
	V _{GE} = 0 V chiplevel	T _j = 150 °C		1.35	1.72	V
V _{F0}	chiplevel	T _j = 25 °C		1.04	1.24	V
		T _j = 150 °C		0.85	0.99	V
r _F	ahiployal	T _j = 25 °C		6.7	9.8	mΩ
	chiplevel	T _j = 150 °C		10	15	mΩ
I _{RRM}	I _F = 50 A	T _j = 150 °C		-		А
Q _{rr}		T _j = 150 °C		-		μC
E _{rr}	V _{CC} = 300 V	T _j = 150 °C				mJ
R _{th(j-c)}	per diode	1			0.81	K/W
R _{th(c-s)}	per diode (λgrease=0.81 W/mK, thickness 50-100μm)			0.082		K/W
R _{th(c-s)}	per diode (λ grease=0.81 W/mK, thickness 50-100 μ m) per diode (λ =3.4 W/mK)			t.b.d.		K/W
Freewhee	ling diode					
$V_F = V_{EC}$	I _F = 300 A	T _j = 25 °C		1.40	1.76	V
	V _{GE} = 0 V chiplevel	T _j = 150 °C		1.39	1.77	V
V _{F0}		T _j = 25 °C	1	1.04	1.236	V
	- chiplevel	T _j = 150 °C	1	0.85	0.99	V
r _F	chiplevel	T _j = 25 °C	1	1.19	1.76	mΩ
		T _j = 150 °C	1	1.79	2.6	mΩ
I _{RRM}	$I_{\rm F} = 300 {\rm A}$	T _j = 150 °C		141.4		Α
Q _{rr}	di/dt _{off} = 2038 A/µs	T _j = 150 °C		28		μC
Err	V _{CC} = 300 V	T _j = 150 °C		4.7		mJ
R _{th(j-c)}			0.23	K/W		
R _{th(c-s)}	per diode (λgrease=0.81 W/mK, thickness 50-100μm)			0.076		K/W
R _{th(c-s)}	per diode (λ=3.4 W	//mK)		t.b.d.		K/W





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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Module						
L _{CE}				30		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.8		mΩ
		T _C = 125 °C		1.1		mΩ
Rth _{(c-s)1}	calculated without	thermal coupling		0.018		K/W
Rth _{(c-s)2}	including thermal coupling, Ts underneath module (λ _{grease} =0.81 W/ (m*K))		t.b.d.			K/W
Rth _{(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			t.b.d.		K/W
Ms	to heat sink (M5)		3		6	Nm
M _t		to terminals (M6)	3		6	Nm
						Nm
w				398		g
Temperat	ure Sensor					•
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)		493 ± 5%			Ω
B _{100/125}	R _(T) =R ₁₀₀ exp[B _{100/125} (1/T-1/T ₁₀₀)]; T[K];			3550 ±2%		к



























Fig. 8: Typ. switching times vs. gate resistor R_G







This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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